

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listing of claims in the application.

**Listing of Claims:**

1. (Previously Presented) A semiconductor device comprising:

a first semiconductor chip having on one main surface thereof a control circuit, a first bonding pad, and a plurality of second bonding pads;

a second semiconductor chip having on one main surface thereof a memory circuit and a third bonding pad and disposed on the one main surface of the first semiconductor chip, the memory circuit being controlled in accordance with a control signal generated in the control circuit on the first semiconductor chip;

a first lead having an inner lead portion and an outer lead portion integral with the inner lead portion, the inner lead portion being disposed at a position around the first semiconductor chip;

a plurality of second leads each having an inner lead portion and an outer lead portion integral with the inner lead portion, the inner lead portion being disposed at a position around the first semiconductor chip;

a first bonding wire for connecting the first bonding pad on the first semiconductor chip with the inner lead portion of the first lead;

a plurality of second bonding wires for connecting the plural second bonding pads on the first semiconductor chip with the inner lead portions of the plural second leads;

a third bonding wire for connecting the third bonding pad on the second semiconductor chip with the inner lead portion of the first lead; and

a resin seal member for sealing the first and second semiconductor chips, the first, second and third bonding wires, and the inner lead portions of the first and second leads,

wherein the control signal generated in the control circuit is outputted from the first bonding pad on the first semiconductor chip and is inputted to the third bonding pad on the second semiconductor chip through the first bonding wire, the first lead and the third bonding wire.

2. (Original) A semiconductor device according to claim 1, wherein the second semiconductor chip is formed in a plane size smaller than that of the first semiconductor chip.

3. (Previously Presented) A semiconductor device according to claim 1, wherein another main surface of the second semiconductor chip opposed to the one main surface thereof is disposed on the one main surface of the first semiconductor chip so as to be facing the one main surface of the first semiconductor chip.

4. (Previously Presented) A semiconductor device according to claim 1,  
wherein the first and third bonding wires are connected to the same surface of the  
first lead.

5-31. (Canceled)